

FIG.2

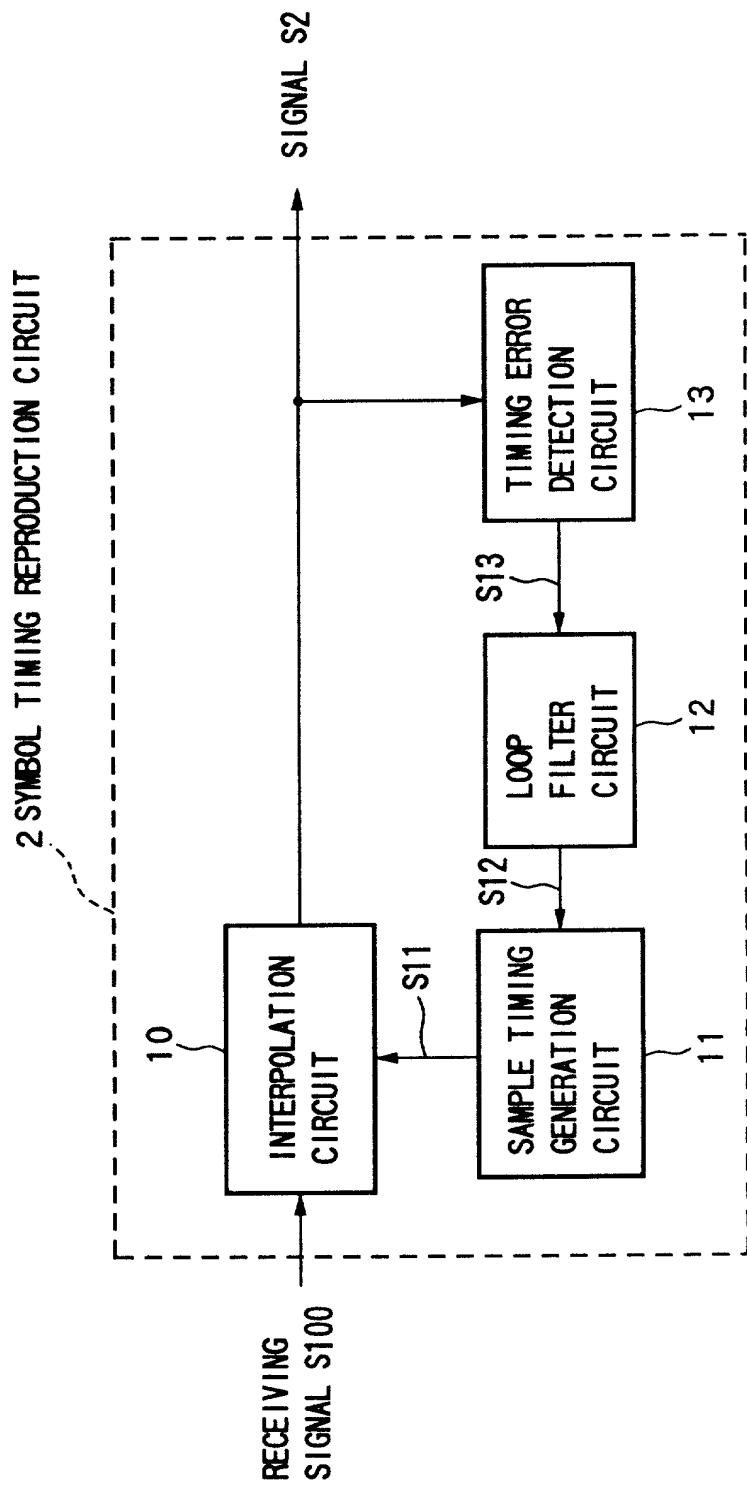


FIG.3A

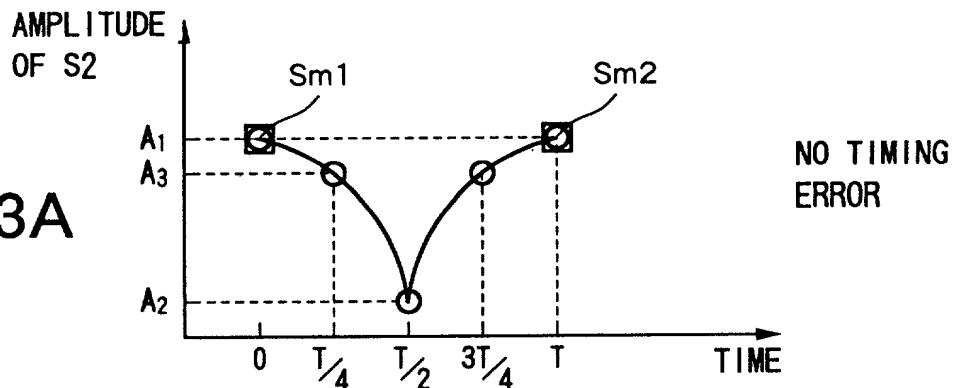


FIG.3B

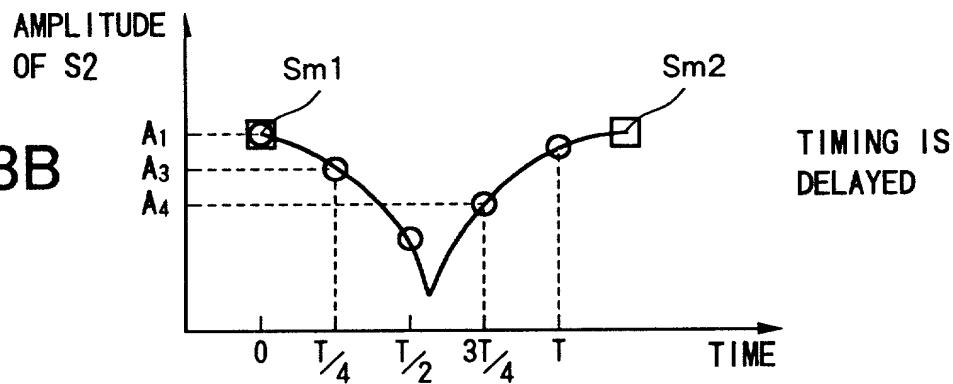
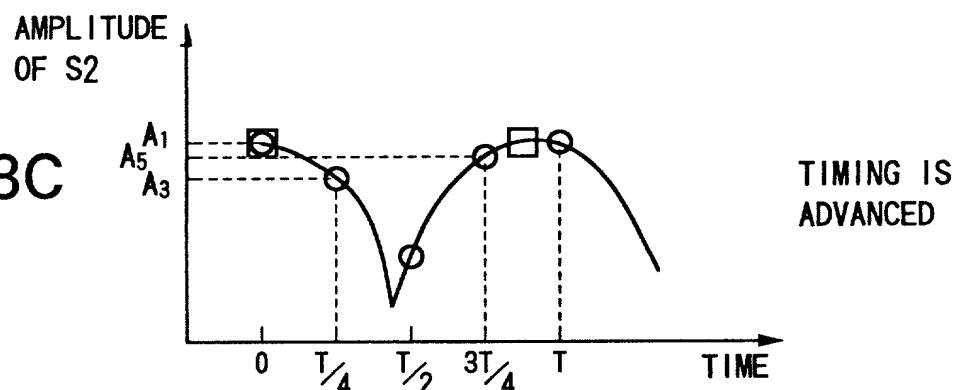


FIG.3C



□ SYMBOL OF S_2
○ SAMPLING POSITION IN
TIMING ERROR DETECTION CIRCUIT

FIG.4

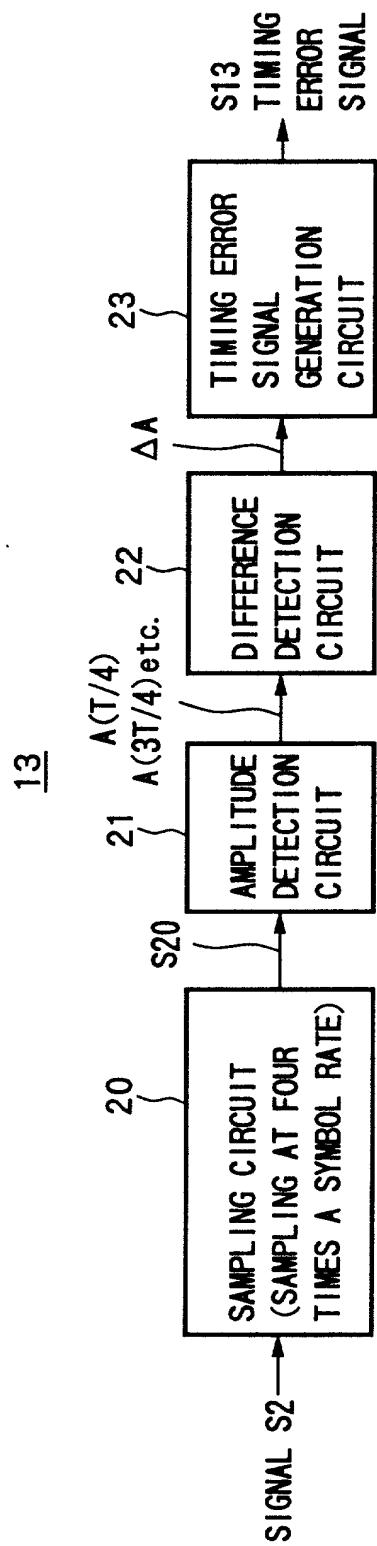


FIG.5

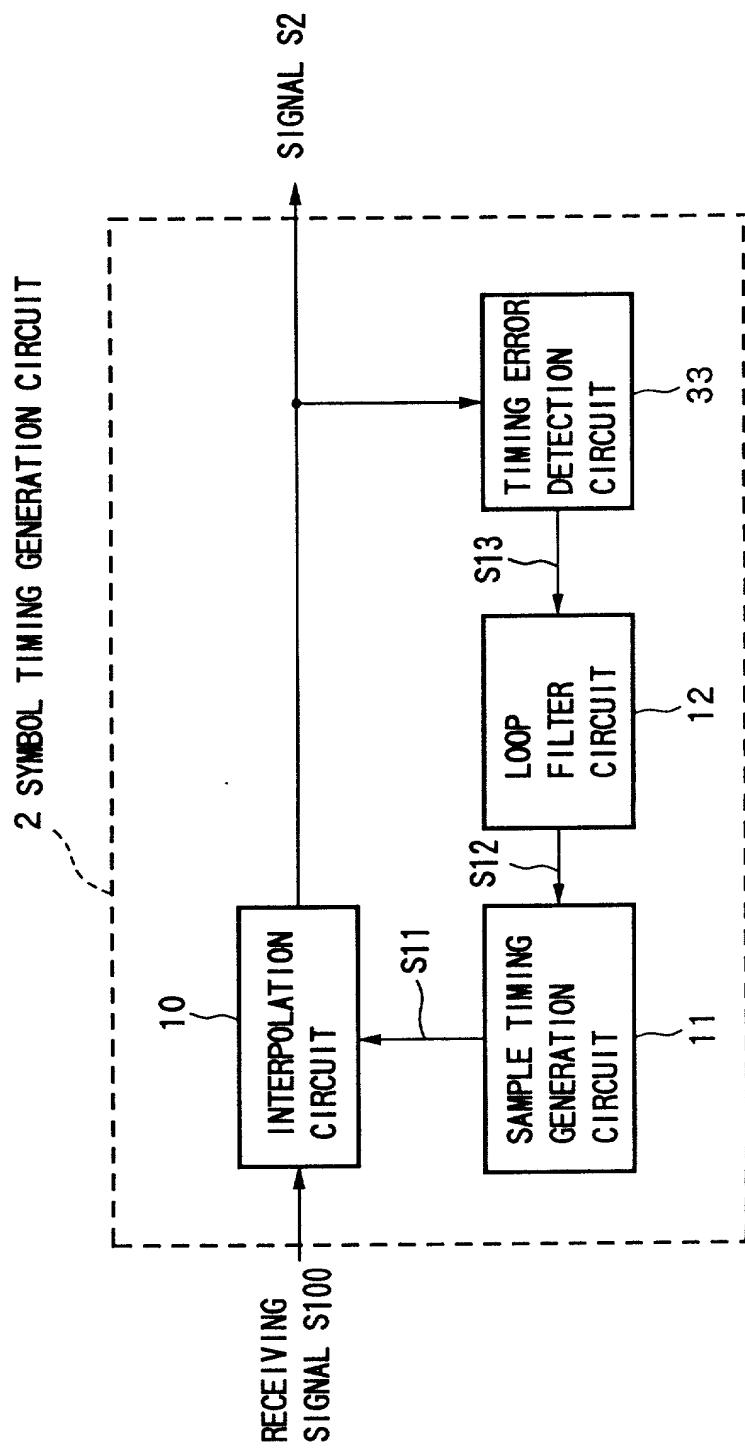


FIG.6A

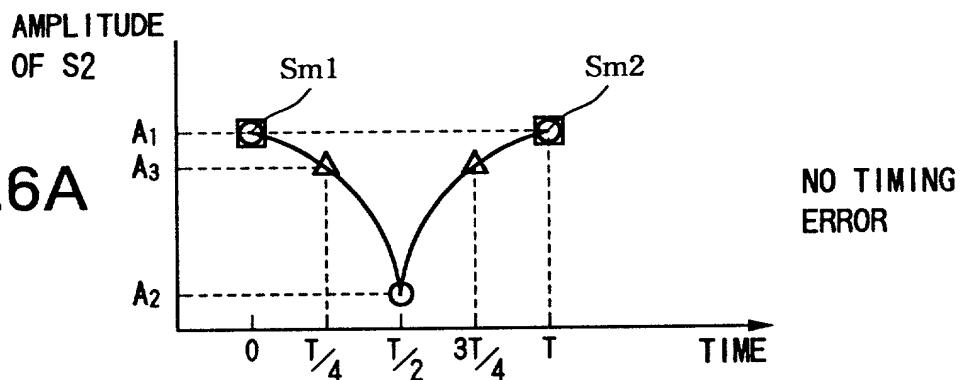


FIG.6B

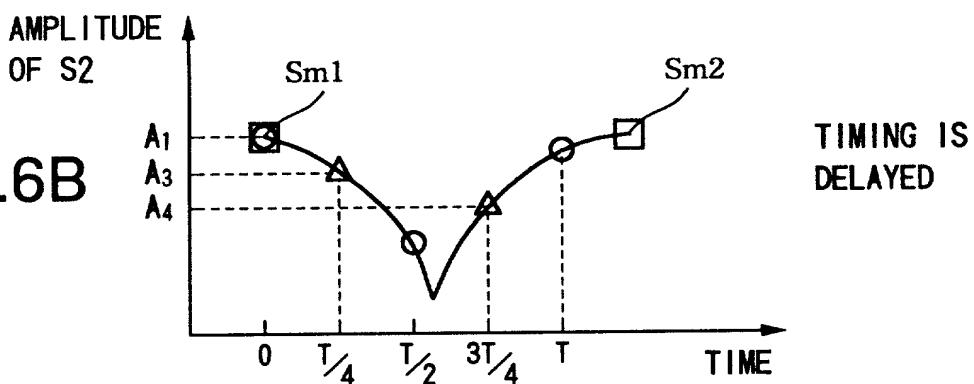
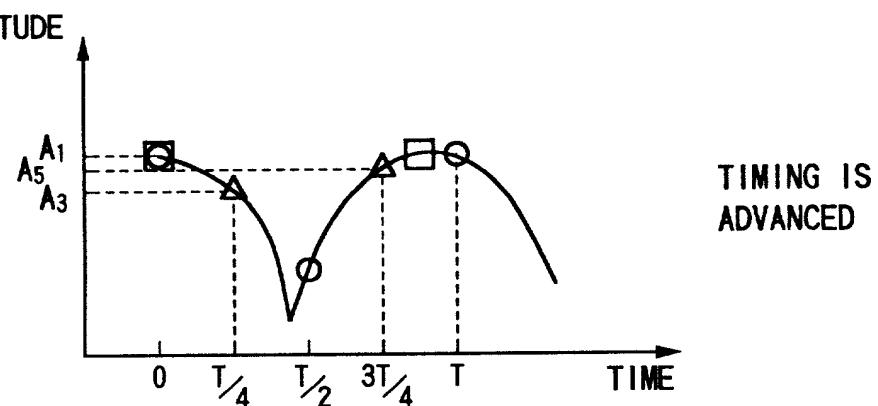


FIG.6C



- SYMBOL OF S_2
- SAMPLING POSITION IN
TIMING ERROR DETECTION CIRCUIT
- △ DATA OBTAINED BY INTERPOLATION PROCESSING

FIG. 7

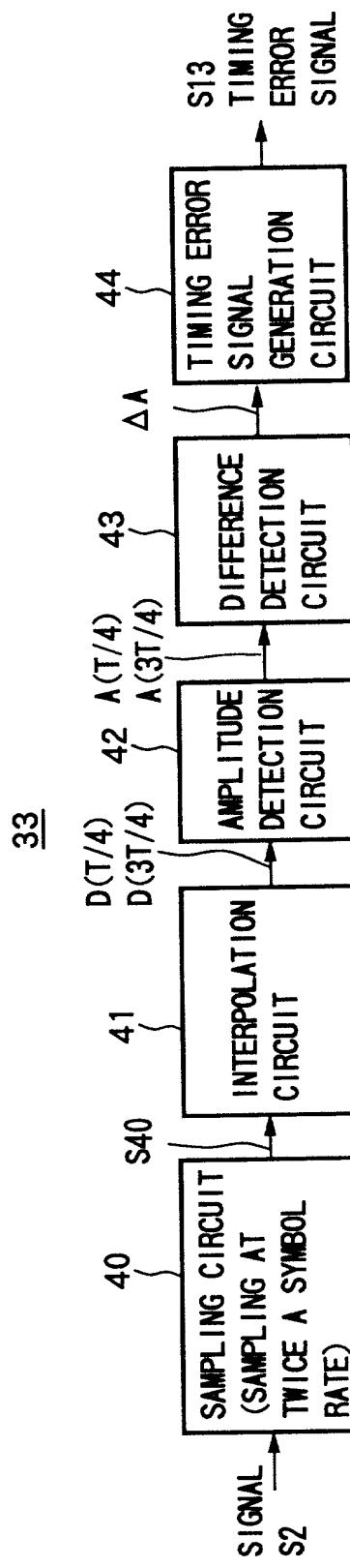
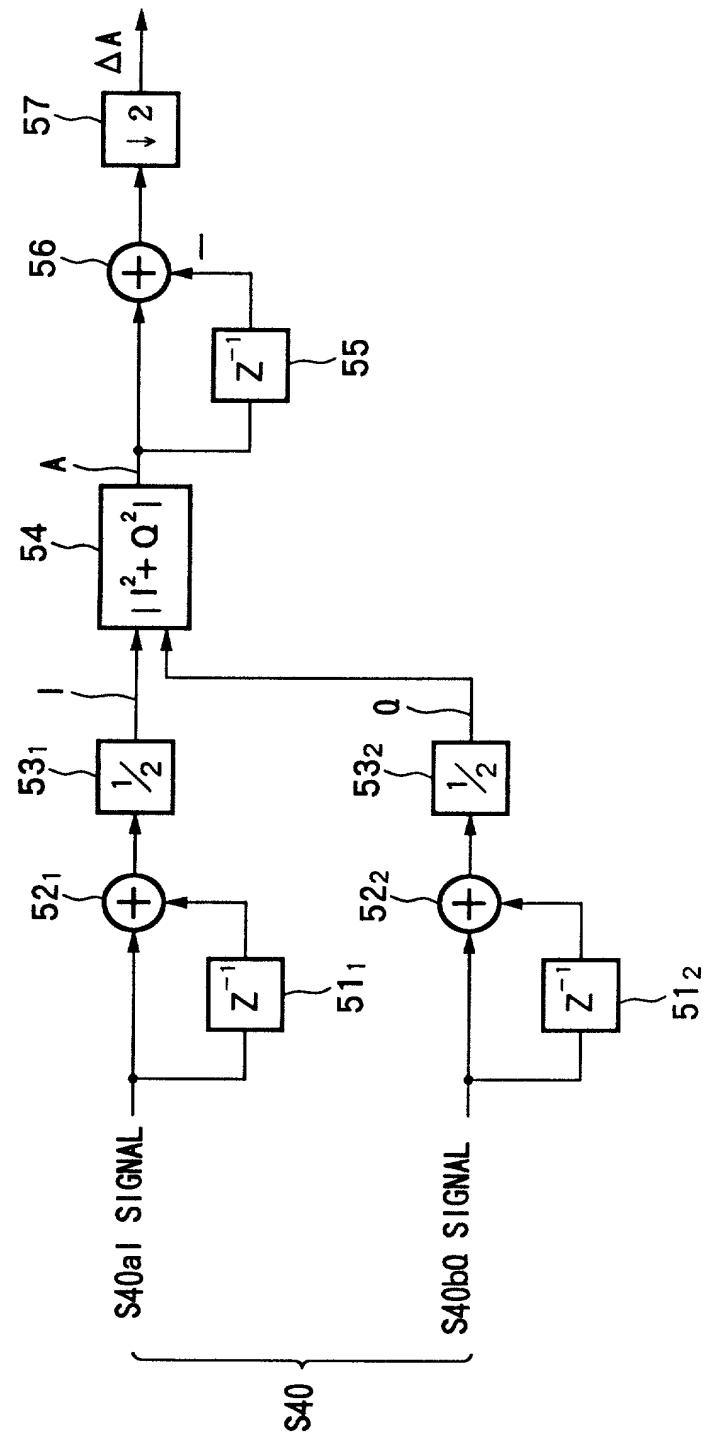


FIG.8



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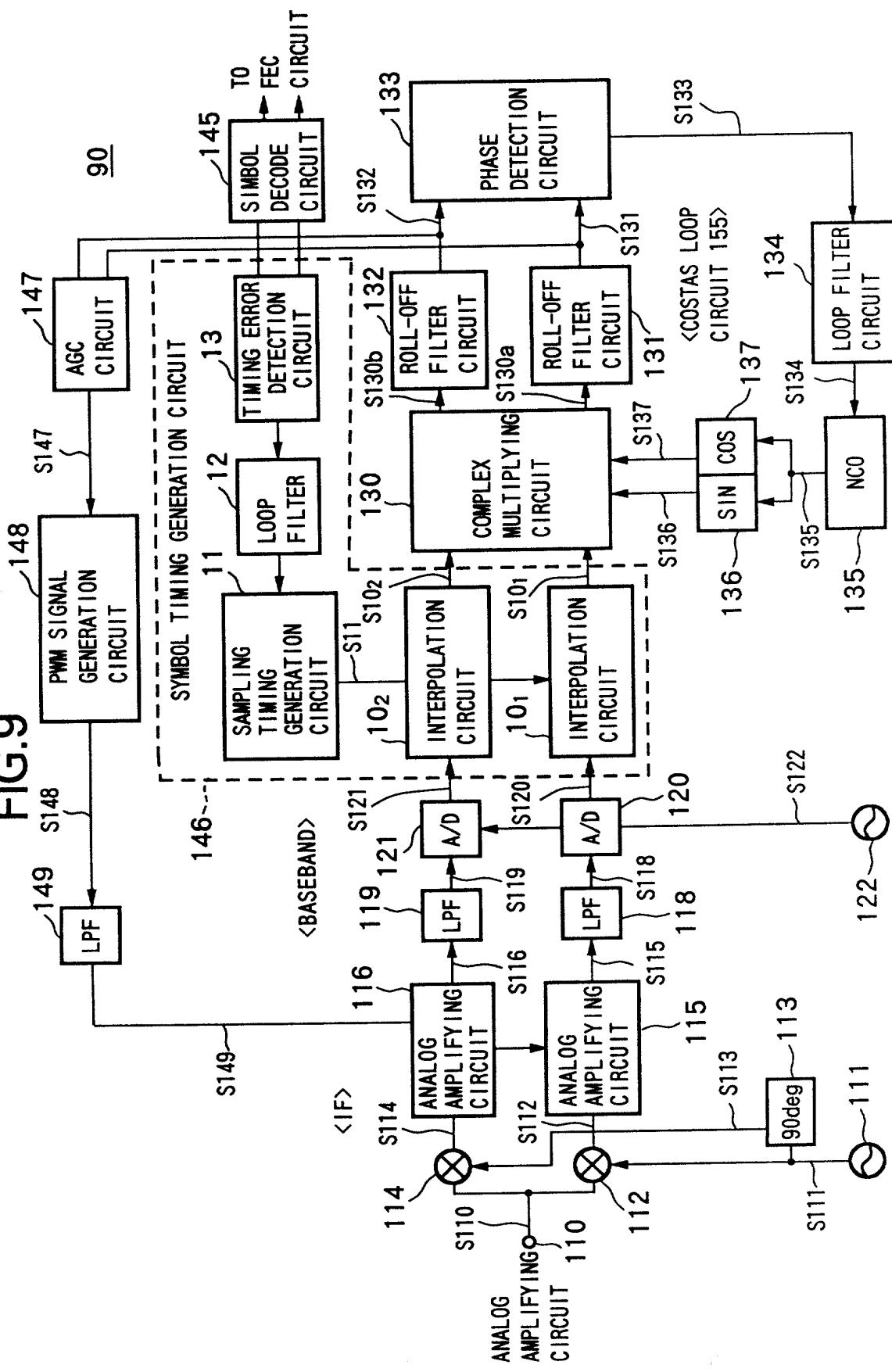


FIG.10

